

Please follow the procedures below to setup the demonstration:

1. Make sure the DE10-Lite is powered off.
2. Connect the LT24 onto the 2x20 GPIO expansion header of the DE10-Lite.
3. Connect the DE10-Lite USB-Blaster USB-B port to the PC USB Port with a USB Cable.
4. Power on the DE10-Lite FPGA Development Board.
5. Make sure Quartus II 16.0 and NIOS II 16.0 are installed on your system.
6. Execute the demo batch file "test.bat" under the batch file folder "demo_batch".
7. Now, you should see the painter GUI on the LCD

