
Thu. 08:20 - 10:00 APP: Sala Eça de Queiroz

(EMBS-1) Embedded Systems: Applications, Solutions, and Techniques
Martino Sykora, Politecnico di Milano

VHDL vs. Bluespec System Verilog: A Case Study on a Java Embedded Architecture

Flavius Gruian, Lund University, Sweden

Mark Westmijze, University of Twente, The Netherlands

Formal Specification of DSP Gateway for Data Transmission Between Processor Cores of OMAP Platform

Saulo Luiz, Federal University of Campina Grande, Brazil

Genildo Vasconcelos, Federal University of Campina Grande, Brazil

Leandro Dias da Silva, Paraíba State University, Brazil

Angelo Perkusich, Federal University of Campina Grande, Brazil

Antonio Lima, Federal University of Campina Grande, Brazil

Hardware Implementation for Network Intrusion Detection Rules with Regular Expression Support

Dan Lo, UTSA, USA

Yi-Gang Tai, UTSA, USA

Kleanthis Psarris, UTSA, USA

LTS-BT: A Tool to Generate and Select Functional Test Cases for Embedded Systems

Emanuela Cartaxo, Federal University of Campina Grande, Brazil

Wilkerson Andrade, Federal University of Campina Grande, Brazil

Francisco Oliveira Neto, Federal University of Campina Grande, Brazil

Patrícia Machado, Federal University of Campina Grande, Brazil

A Hybrid Software-Based Self-Testing

Methodology for Embedded Processor

Tai-Hua Lu, National Cheng Kung University, Taiwan

Chung-Ho Chen, National Cheng Kung University, Taiwan

Kuen-Jong Lee, National Cheng Kung University, Taiwan

Thu. 10:30 - 12:10 APP: Sala Eça de Queiroz

(EMBS-2) Embedded Systems: Applications, Solutions, and Techniques
Alessio Bechini, University of Pisa

Exploiting Program Cyclic Behavior to Reduce Memory Latency in Embedded Processors

Ehsan Atoofian, University of Victoria, Canada
Amirali Baniasadi, University of Victoria, Canada

The Minimization of Hardware Size in Reconfigurable Embedded Platforms

Nei-Chiung Perng, Genesys Logic, Taiwan
Jian-Jia Chen, Swiss Federal Institute of Technology, Switzerland
Tei-Wei Kuo, National Taiwan University, Taiwan
Chi-Sheng Shih, National Taiwan University, Taiwan

Dynamic Configuration of Application-Specific Implicit Instructions for Embedded Pipelined Processors

Martino Sykora, Politecnico di Milano, Italy
Giovanni Agosta, Politecnico di Milano, Italy
Cristina Silvano, Politecnico di Milano, Italy

Power-efficient and Scalable Load/Store Queue Design via Address Compression

Yi Ying Tsai, National Cheng Kung University, Taiwan
Chia-Jung Hsu, National Cheng Kung University, Taiwan
Chung-Ho Chen, National Cheng Kung University, Taiwan

PFFS: A Scalable Flash Memory File System for the Hybrid Architecture of Phase-Change RAM and NAND Flash

Youngwoo Park, KAIST, Korea
Seung-Ho Lim, KAIST, Korea
Chul Lee, KAIST, Korea
Kyu Ho Park, KAIST, Korea
Jin Kyu Kim, Samsung Electronics Co., LTD., Korea
Kyoungil Bahng, Samsung Electronics Co., LTD., Korea

Thu. 13:30 - 15:10 APP: Sala Eça de Queiroz

(EMBS-3) Embedded Systems: Applications, Solutions, and Techniques
Alessio Bechini, University of Pisa

Flash Memory Management Based on Predicted Data Expiry-Time in Embedded Real-time Systems

Peng Wei, University of Science and Technology of China, P.R.China
Lihua Yue, University of Science and Technology of China, P.R.China
Zhanzhan Liu, University of Science and Technology of China, P.R.China
Xiaoyan Xiang, University of Science and Technology of China, P.R.China

Hierarchical Memory System Design for A Heterogeneous Multi-core Processor

Jianjun Guo, National University of Defense Technology, China
Mingche Lai, National University of Defense Technology, China
Zhengyuan Pang, National University of Defense Technology, China
Libo Huang, National University of Defense Technology, China
Fangyuan Chen, National University of Defense Technology, China
Kui Dai, National University of Defense Technology, China
Zhiying Wang, National University of Defense Technology, China

A Reliable B-Tree Implementation over Flash Memory

Xiaoyan Xiang, University of Science and Technology of China, P.R.China
Lihua Yue, University of Science and Technology of China, P.R.China
Zhanzhan Liu, University of Science and Technology of China, P.R.China
Peng Wei, University of Science and Technology of China, P.R.China

Tuesday, 14:00 - 17:00 Sala Vinicius de Moraes
Poster Session 2

(EMBS) Embedded Systems: Applications, Solutions, and Techniques

Control Flow Checking and Recovering based on 8051 Architecture

Rui Gong, National University of Defense Technology, P. R. China
Wei Chen, National University of Defense Technology, P. R. China
Fang Liu, National University of Defense Technology, P. R. China
Kui Dai, National University of Defense Technology, China
Zhiying Wang, National University of Defense Technology, China

Using Traditional Loop Unrolling to Fit Application on a New Hybrid Reconfigurable Architecture

Monica Magalhães Pereira, Federal University of Rio Grande do Norte, Brazil
Sílvia R. F. de Araújo, Federal University of Rio Grande do Norte, Brazil
Bruno C. de Oliveira, Federal University of Rio Grande do Norte, Brazil
Ivan Saraiva Silva, Federal University of Rio Grande do Norte, Brazil

Filtering Drowsy Instruction Cache to Achieve Better Efficiency

Roberto Giorgi, University of Siena, Italy
Paolo Bennati, University of Siena, Italy
