Formal Methods

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FMSS, 2019-2020

Overview

Relation between dependability and security

- Formal methods definition
- Specification languages
- Verification techniques
 - model checking
 - abstract interpretation
 - theorem proving
- Case studies:
 - Malware analysis
 - Data leakage
 - Cyber-physical systems attacks

Formal methods are mathematically-based techniques that can be used in the design and development of computer-based systems.

Formal methods

- allow the analysis of all possible executions of the system
- improve the current techniques based on simulation and testing (mathematical proof that the system behaves as expected)
- offer the possibility for detecting vulnerability in systems or for building more secure systems by design.

A formal method consists of

 a language a mathematical notation or a computer language with a formal semantics

- a set of tools for proving properties of the system
- a methodology for its application in industrial practice.

Formal methods for modelling and verification

- formal methods for the analysis of systems
- formal methods for the analysis of programs

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Formal methods for the analysis of programs

- Semantics of the programming language
- Control flow graph of a program

The semantics of the language describes mathematically the behaviour of the program.

The control flow graph represents the control structure of the program.

In the following:

- structured high level language (e.g., C language)
- Iow level languages (e.g, Java bytecode, assembly code)

A simple high level language

We consider a simple sequential language with the following syntax, where *op* stands for the usual arithmetic and logic operations

Instruction set

exp::= const | var | exp op exp
com: = var := exp | if exp then com else com |
while exp do com | com; com | skip

A program *P* is a sequence of instructions $\langle c \rangle$, where $c \in com$.

The control flow graph of a program $P = \langle c \rangle$ is a directed graph (*V*; *E*), where *V* is a set of nodes and *E* : *VxV* is a set of edges connecting nodes.

Nodes correspond to instructions. Moreover, there is an initial node and a final node that represent the starting point and the final point of an execution. E contains the edge (i; j) if and only if the instruction at address j can be immediately executed after that at address i.

The control flow graph does not contain information on the semantics of the instructions.

An example



C++ generation of the CFG with Visual Studio. Gcc developer-options: -fdump-tree-cfg -blocks -vops

An example





:

State of the program:

x 2 y 5 z 3 k 4 memory m

\$\langle c, m \rangle\$
 where c is a command and m is a memory

► ⟨*m*⟩

a single memory, in the final state.

The semantics of programs is given by means of a transition system and we call this semantics *execution semantics*.

- ► Var(c) denote the set of variables occurring in c.
- ▶ \mathcal{V}^{ϵ} is the domain of constant values, ranged over by k, k', ...,
- ▶ for each $X \subseteq var$, the domain $\mathcal{M}_X^{\epsilon} = X \to \mathcal{V}^{\epsilon}$ of memories defined on X, ranged over by m, m', \ldots

Transitions

The semantic rules define a relation

 $\longrightarrow^{\epsilon} \subseteq \mathcal{Q}^{\epsilon} \times \mathcal{Q}^{\epsilon}$

where \mathcal{Q}^{ϵ} is a set of states.

A separate transition

$$\longrightarrow_{\textit{expr}}^{\epsilon} \subseteq (\textit{exp} imes \mathcal{M}^{\epsilon}) imes \mathcal{V}^{\epsilon}$$

is used to compute the value of the expressions.

With m[k/x] we denote the memory m' which agrees with m on all variables, except on x, for which m'(x) = k.

The symmetric rule for the conditional command (false condition) are omitted.

Operational semantics

Expr_{const}
$$\overline{\langle k, m \rangle \longrightarrow_{expr}^{\epsilon} k}$$

Expr_{var}
$$\langle x, m \rangle \longrightarrow_{expr}^{\epsilon} m(x)$$

$$\mathsf{Expr}_{op} \quad \frac{\langle e_1, m \rangle \longrightarrow_{expr}^{\epsilon} k_1 \ \langle e_2, m \rangle \longrightarrow_{expr}^{\epsilon} k_2 \ k_1 \ op \ k_2 = k_3}{\langle (e_1 \ op \ e_2), m \rangle \longrightarrow_{expr}^{\epsilon} k_3}$$

Ass
$$\frac{\langle e, m \rangle \longrightarrow_{expr}^{\epsilon} k}{\langle x := e, m \rangle \longrightarrow^{\epsilon} m[k/x]}$$

Skip
$$\overline{\langle \text{skip}, m \rangle \longrightarrow^{\epsilon} \langle m \rangle}$$

Operational semantics

While true
$$\langle e, m \rangle \longrightarrow_{expr}^{\epsilon} true$$

 $\langle while e do c, m \rangle \longrightarrow^{\epsilon} \langle c; while e do c, m \rangle$

While
$$_{false} \xrightarrow{\langle e, m \rangle \longrightarrow_{expr}^{\epsilon} false} \langle while e do c, m \rangle \longrightarrow^{\epsilon} \langle m \rangle}$$

$$\mathbf{Seq}_1 \quad \frac{\langle c_1, m \rangle \longrightarrow^{\epsilon} \langle m' \rangle}{\langle c_1; c_2, m \rangle \longrightarrow^{\epsilon} \langle c_2, m' \rangle} \qquad \qquad \mathbf{Seq}_2 \quad \frac{\langle c_1, m \rangle \longrightarrow^{\epsilon} \langle c_2, m' \rangle}{\langle c_1; c_3, m \rangle \longrightarrow^{\epsilon} \langle c_2; c_3, m' \rangle}$$

Given a program $P = \langle c \rangle$ and an initial memory $m \in \mathcal{M}^{\epsilon}_{Var(c)}$, we denote by E(P, m)

the transition system defined by \rightarrow^{ϵ} starting from the initial state $\langle c, m \rangle$.

Actually, since the program is deterministic, there exists at most one final state, i.e. a state $\langle m \rangle$ for some memory *m*.

Java language

```
public class Hello {
3
    public static void main(String[] args) {
4
5
6
    public void stampa(String s){
           System.out.println("Outside");
8
9
      try { System.out.println("Inside try");}
10
      catch (ArithmeticException e) {System.out.println("Print catch");}
11
12
     System.out.println("Print ....");
13
14
15
```

Java bytecode

```
1 Compiled from "Hello, java"
2 public class Hello {
    public Hello();
      Code:
         0: aload_0
         1: invokespecial #1 // Method java/lang/Object."<init >":()V
         4: return
    public static void main(java.lang.String[]);
9
      Code:
10
         0: return
    public void stampa(java.lang.String);
      Code:
14
         0: getstatic #2 // Field java/lang/System.out: Ljava/io/ PrintStream;
         3: 1dc
                       #3 // String Outside
16
         5: invokevirtual #4 // Method java/io/ PrintStream. println:(Ljava/lang/String:)V
17
         8: getstatic #2 // Field java/lang/System.out: Ljava/io/PrintStream;
18
        11: 1dc
                           #5 // String Inside try
19
        13: invokevirtual #4 // Method java/io/PrintStream.println:(Ljava/lang/String;)V
20
        16: goto
                           28
        19: astore_2
        20: getstatic
                           #2 // Field java/lang/System.out: Ljava/io/PrintStream;
23
                           #7 // String Print catch
        23: Idc
24
        25: invokevirtual #4 // Method java/io/PrintStream.println:(Ljava/lang/String;)V
25
        28: getstatic
                           #2 // Field java/lang/System.out: Ljava/io/PrintStream;
26
        31: 1dc
                           #8 // String Print ...
27
        33: invokevirtual #4 // Method java/io/ PrintStream. println:(Ljava/lang/String;)V
28
        36: return
29
30
```

Java bytecode

Instruction set

рор	Pop top operand stack element.	
dup	Duplicate top operand stack element.	
α ορ	Pop two operands with type $lpha$ off the operand stack,	
	perform the operation $op \in \{ \text{ add, mult, compare } \}$,	
	and push the result onto the stack.	
$lpha$ const ${\pmb d}$	Push constant <i>d</i> with type α onto the operand stack.	
$lpha$ load $oldsymbol{x}$	Push the value with type α of the register x	
	onto the operand stack.	
$lpha$ store ${\it X}$	Pop a value with type α off the operand stack and	
	store it into local register x.	
if cond j	Pop a value off the operand stack, and evaluate it against	
-	<pre>the condition cond = { eq, ge, null, };</pre>	
	branch to <i>j</i> if the value satisfies <i>cond</i> .	
goto j	Jump to j.	

Java bytecode Instruction set

getfield C .f	Pop a reference to an object of class C
	off the operand stack; fetch the object's
	field <i>f</i> and put it onto the operand stack.
putfield C .f	Pop a value k and a reference to an
	object of class C from the operand stack;
	set field f of the object to k.
invoke C . <i>mt</i>	Pop value k and a reference r to an
	object of class C from the operand stack;
	invoke method C.mt of the referenced
	object with actual parameter k.
lphareturn	Pop the α value off the operand stack and return it
	from the method.

The bytecode of a method is a sequence *B* of instructions.

When a method is invoked (invoke instruction), it executes with a new empty stack and with an initial memory where all registers are undefined except for the first one, register x0, that contains the reference to the object instance on which the method is called, and register x1, that contains the actual parameter.

When the method returns, control is transferred to the calling method: the caller's execution environment (operand stack and local registers) is restored and the returned value, if any, is pushed onto the operand stack.

An example

The bytecode corresponds to a method *mt* of a class A. Suppose that register x1 (the parameter of *A.mt*) contains a reference to an object of another class B. Note that register x0 contains a reference to A. After the bytecode has been executed, the final value of field f1 of the object of class A is 0 or 1 depending on the value of field f2 of the object of class B.

0:	aload	<i>x</i> 0
1:	aload	<i>x</i> 1
2 :	getfield	B.f2
3:	ifge	6
4:	iconst	0
5 :	goto	7
6:	iconst	1
7:	putfield	A.fl
8:	iconst	1
9:	return	

A code and its CFG

0:
1: load x
2: ifge 5
3: iconst_1
4: istore_2
5: goto 6
6:



$$\begin{array}{c} \mathbf{op} & \frac{c[i] = \mathbf{op}}{\langle i, m, k_1 \cdot k_2 \cdot s \rangle \longrightarrow^e \langle i + 1, m, (k_1 \ op \ k_2) \cdot s \rangle} \\ \mathbf{pop} & \frac{c[i] = \mathbf{pop}}{\langle i, m, k \cdot s \rangle \longrightarrow^e \langle i + 1, m, s \rangle} \\ \mathbf{push} & \frac{c[i] = \mathbf{push} \ k}{\langle i, m, s \rangle \longrightarrow^e \langle i + 1, m, k \cdot s \rangle} \\ \mathbf{load} & \frac{c[i] = \mathbf{load} \ x}{\langle i, m, s \rangle \longrightarrow^e \langle i + 1, m, m(x) \cdot s \rangle} \\ \mathbf{store} & \frac{c[i] = \mathbf{store} \ x}{\langle i, m, k \cdot s \rangle \longrightarrow^e \langle i + 1, m[k/x], s \rangle} \end{array}$$

$$\begin{array}{c} \mathbf{if}_{false} & \frac{c[i] = \mathrm{if} \mathrm{j}}{\langle i, m, 0 \cdot s \rangle \longrightarrow^{e} \langle i + 1, m, s \rangle} \\ \mathbf{if}_{true} & \frac{c[i] = \mathrm{if} \mathrm{j}}{\langle i, m, k \neq 0 \cdot s \rangle \longrightarrow^{e} \langle j, m, s \rangle} \\ \mathbf{goto} & \frac{c[i] = \mathrm{goto} \mathrm{j}}{\langle i, m, s \rangle \longrightarrow^{e} \langle j, m, s \rangle} \end{array}$$

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