Voltage references

Voltage references are blocks that produce an output voltage that is independent of **PVT** variations:

V: Supply voltage

T: Temperature

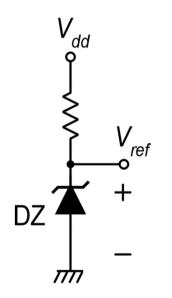
P: Process errors

Voltage references are used for:

- Providing an absolute reference voltage for ADCs and DACs
- Providing an absolute reference voltage for stimulating sensors or other external devices that require precise control voltages and/or currents.
- Creating constant bias voltages (and currents) when required

Possible reference voltage sources

Zener Diodes



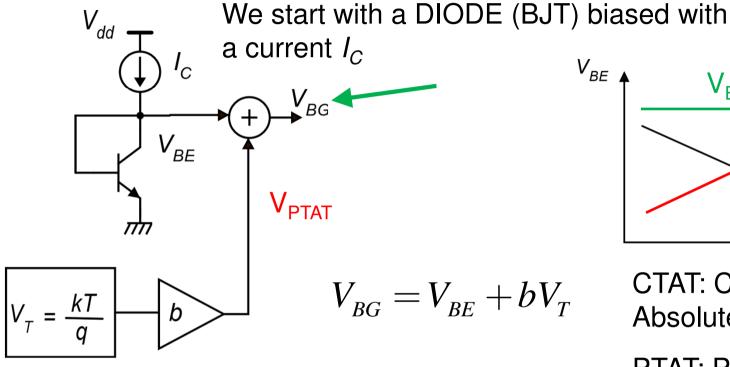
Problems:

- > Require additional process steps, but only a small number of components are required for each chip (not convenient)
- Available voltages are > 3 V
- \triangleright Temperature stability is poor for $V_7 \neq 5-6$ V
- > The reference voltage generated by a Zener diode is noisy (very wide band noise)

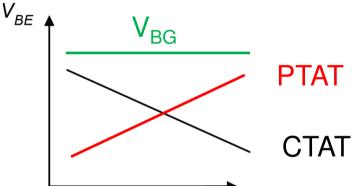
In present days, zener diodes are available only in high voltage processes and are used more for protection than for voltage references

Band-gap circuits soluti

Band-gap voltage reference: principle of operation



 $\frac{dV_{BE}}{dT} \cong -2 \text{ mV/K } \dots -3 \text{ mV/K}$



CTAT: Complementary To Absolute Temperature

PTAT: Proportional To Absolute Temperature

Band-gap voltage reference: determination of parameter b and estimate of the output voltage

$$V_{BG} = V_{BE} + bV_{T}$$

We have to determine the value of *b*, for which:

$$\frac{dV_{BG}}{dT} = 0$$

$$\frac{dV_{BG}}{dT} = \frac{dV_{BE}}{dT} + b\frac{dV_{T}}{dT} = 0$$

$$\frac{dV_T}{dT} = \frac{k}{q} \cong 8.56 \times 10^{-5} \ V / K$$

$$b = \frac{-\frac{dV_{BE}}{dT}}{\frac{dV_{T}}{dT}}$$

Good news! This voltage is compatible with low-supply voltage circuits

Example:
$$\frac{dV_{BE}}{dT} \cong -2 \text{ mV/K} \Rightarrow b \cong 23$$

$$V_{BE}$$
 V_{T} (290 K)
 $V_{BG} \cong 0.65 + 0.025 \times 23 = 1.225 \text{ V}$

$$V_{BG} \cong 0.65 + 0.025 \times 23 = 1.225 \text{ V}$$

Band-Gap voltage reference: theory

$$V_{BE} = V_T \ln \left(\frac{I_C}{I_S}\right) \qquad I_S = \frac{qA_E n_i^2 D_n}{Q_B} = F \cdot n_i^2 D_n$$

$$I_S = \frac{qA_E n_i^2 D_n}{Q_B} = F \cdot n_i^2 D_n$$

$$I_S = BT^{\gamma} e^{-\frac{V_{G0}}{V_T}} \qquad \mu_n \propto T^{-\alpha_\mu}$$

$$V_{G0}$$

$$Constant$$

 $V_{dd} = I_C = GT^{\alpha}$ It is not necessary that I_C is temperature-independent

$$V_{BE} \qquad V_{BE} = V_T \ln \left(\frac{GT^{\alpha}}{BT^{\gamma} \exp\left(\frac{-V_{GO}}{V_T}\right)} \right)$$

$$E = \frac{1}{B}$$

$$= V_{GO} + V_T \left[\ln (G \cdot E) - (\gamma - \alpha) \ln (T) \right]$$

Gray, Hurst, Lewis, Meyer, "Analysis and design of analog integrated circuits" 4th edition, 2001 J.Wiley & Sons

Band-Gap voltage reference: theory

$$V_{BE} = V_{GO} + V_T \left[\ln (G \cdot E) - (\gamma - \alpha) \ln (T) \right] \qquad V_{BG} = V_{BE} + bV_T$$

$$V_{BG} = V_{GO} + V_T \left[\ln (G \cdot E) + b - (\gamma - \alpha) \ln (T) \right] = V_{GO} + \frac{kT}{q} \left[\ln (G \cdot E) + b - (\gamma - \alpha) \ln (T) \right]$$

The name "band-gap" of this reference voltage comes from V_{GO} , which is the dominant part

Let us calculate the derivative of V_{BG} with respect to temperature

$$\frac{dV_{BG}}{dT} = \frac{k}{q} \left[\ln \left(G \cdot E \right) + b - \left(\gamma - \alpha \right) \ln \left(T \right) \right] - \left(\gamma - \alpha \right) \frac{kT}{q} \frac{1}{T}$$

$$\frac{dV_{BG}}{dT} = \frac{k}{q} \left[\ln \left(G \cdot E \right) + b - \left(\gamma - \alpha \right) - \underbrace{\left(\gamma - \alpha \right) \ln \left(T \right)} \right]$$

 $V_{G0} = \frac{E_{g0}}{q}$ V_{GO} is numerically equivalent to E_{g0} measured in eV

$$E_{g0} \cong 1.2 \ eV \implies V_{G0} \cong 1.2 \ V$$

The derivative of V_{BG} depends on temperature

Band-Gap voltage reference: theory

We impose that the derivative of V_{BG} is zero at a given temperature T_0 . This is possible, since b is a free parameter that can be chosen to obtain this result.

$$\frac{k}{q} \Big[\ln (G \cdot E) + b - (\gamma - \alpha) - (\gamma - \alpha) \ln (T_0) \Big] = 0$$

$$\ln(G \cdot E) + b = (\gamma - \alpha) + (\gamma - \alpha) \ln(T_0)$$

$$V_{BG} = V_{GO} + V_T \left[\ln \left(G \cdot E \right) + b - \left(\gamma - \alpha \right) \ln \left(T \right) \right]$$

$$V_{BG} = V_{GO} + V_T \left[(\gamma - \alpha) + (\gamma - \alpha) \ln(T_0) - (\gamma - \alpha) \ln(T) \right]$$

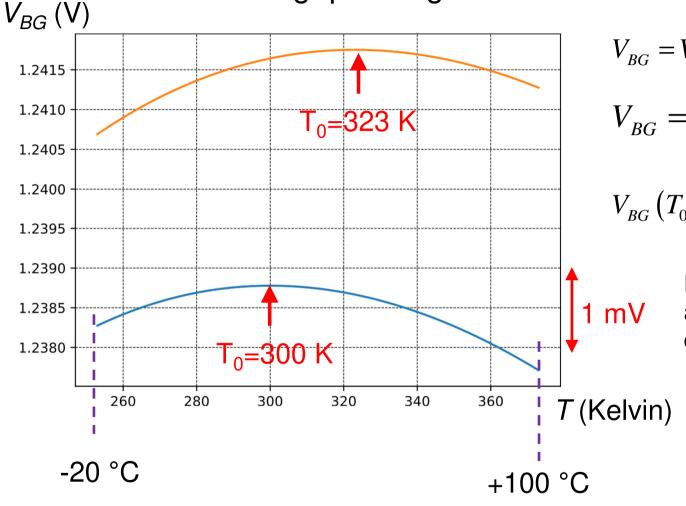
Typically:
$$\alpha=1$$

$$V_{BG} = V_{G0} + V_{T} \left(\gamma - \alpha \right) \left(1 + \ln \left(\frac{T_{0}}{T} \right) \right)$$

$$V_{BG}(T_0) = V_{G0} + \frac{kT_0}{q} (\gamma - \alpha) \approx 1.24 \text{ V}$$

$$\gamma \approx 2.5$$

Band-gap voltage reference: calculation result



$$V_{BG} = V_{BE} + bV_{T}$$

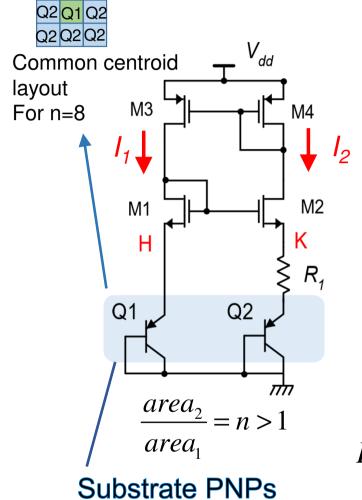
$$V_{BG} = V_{G0} + V_{T}(\gamma - \alpha) \left(1 + \ln\left(\frac{T_{0}}{T}\right)\right)$$

$$V_{BG}(T_{0}) = V_{G0} + \frac{kT_{0}}{\alpha}(\gamma - \alpha)$$

Even if the derivative is zero only at T_0 , the total voltage variation is only a few mV for a wide range

A bandgap voltage reference designed for a higher T_0 , will also have a higher output voltage.

Band-Gap voltage reference: a CMOS compatible Circuit



Q2 Q2 Q2

Part 1: PTAT current generator

$$M3 = M4 \Rightarrow I_1 = I_2 = I \qquad \text{neglecting the effects}$$
 of V_{DS} on I_D :
$$V_{GS1} = V_{GS2}$$

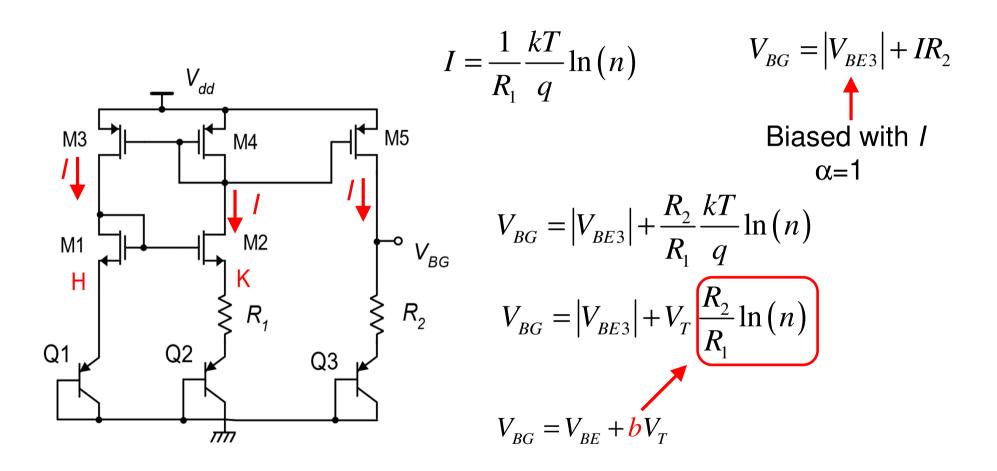
$$V_H - V_K = (V_{G1} - V_{GS1}) - (V_{G2} - V_{GS2}) = V_{GS2} - V_{GS1}$$

$$V_H = |V_{BE1}| \qquad V_K = |V_{BE2}| + R_1 I \qquad V_H = V_K$$

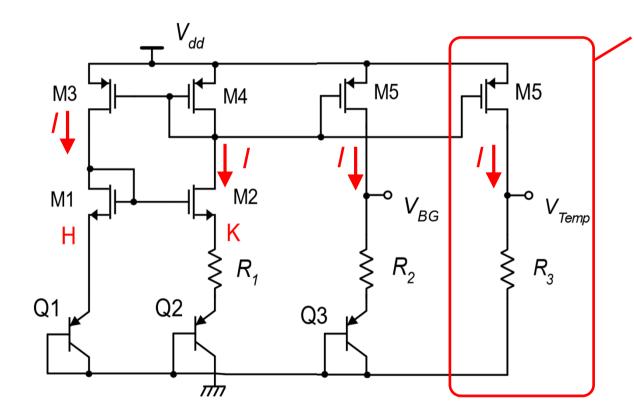
$$R_1 I = |V_{BE1}| - |V_{BE2}| = V_T \ln\left(\frac{I_{S1}}{I_{S1}}\right) = V_T \ln(n)$$

$$I = \frac{1}{R_1} \frac{kT}{q} \ln(n) \qquad I \text{ is proportional to } T \text{ (PTAT) and independent of } V_{dd}.$$

Band-Gap voltage: a CMOS compatible Circuit



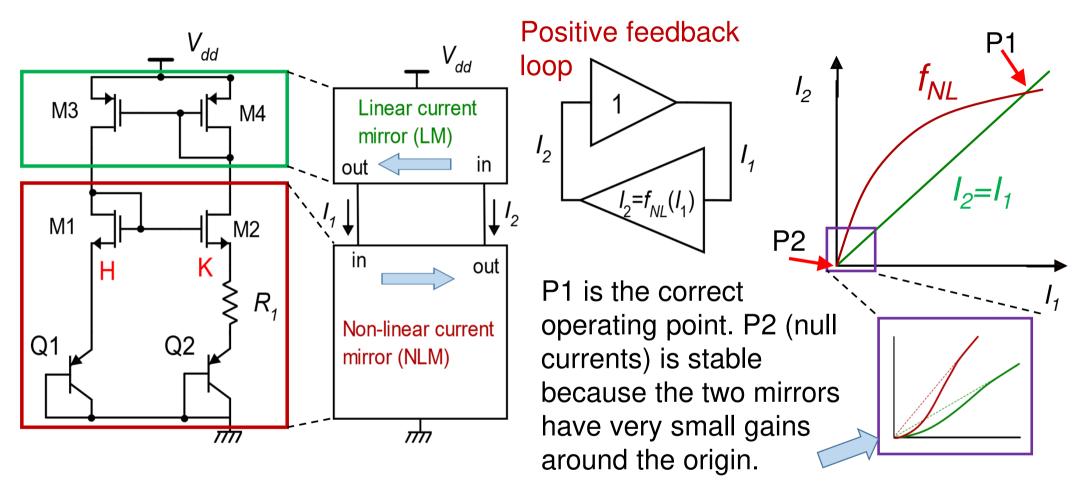
Deriving a temperature sensor from the Band-Gap circuit



Adding this branch, we can obtain a voltage proportional to the absolute temperature, which can be conveniently used to monitor the chip temperature.

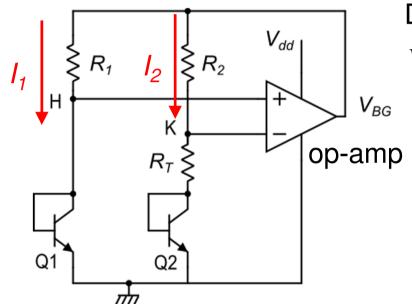
$$V_{Temp} = R_3 I = \frac{R_3}{R_1} \frac{kT}{q} \ln(n)$$

PTAT current generator: multiple stable states



A start-up circuit is necessary to prevent the circuit from being trapped into P2

Mention to another very popular bandgap circuit



Due to virtual short circuit:

$$V_H = V_K \implies V_{R1} = V_{R2}$$
 (voltages across R_1 and R_2)

We choose
$$R_1 = R_2 \implies I_1 = I_2$$

$$V_H = V_K \implies V_{BE1} = V_{BE2} + I_2 R_T$$

$$V_{H} = V_{K} \implies V_{BE1} = V_{BE2} + I_{2}R_{T}$$

$$I_{C1} \cong I_{1}$$

$$I_{C2} \cong I_{2}$$

$$I_{2}R_{T} = V_{BE1} - V_{BE2} = V_{T} \ln \left(\frac{I_{C1}}{I_{S1}} \frac{I_{S2}}{I_{C2}}\right)$$

$$n = \frac{I_{S2}}{I_{S1}}$$

$$\int_{C_1} I_{C_1} \cong I_1$$

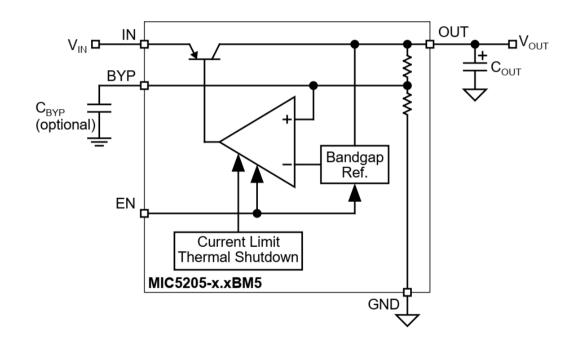
$$I_{C_2} \cong I_2$$

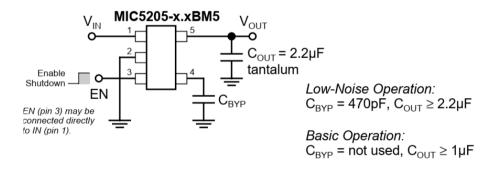
$$n = \frac{I_{S2}}{I_{S1}}$$

$$I_{2} = I_{1} = \frac{V_{T} \ln(n)}{R_{T}}$$
 $V_{BG} = V_{BE1} + I_{1}R_{1} = V_{BE1} + \frac{V_{T} \ln(n)}{R_{T}}R_{1}$

The bandgap voltage reference in voltage regulators







Ultra-Low-Noise Fixed Regulator