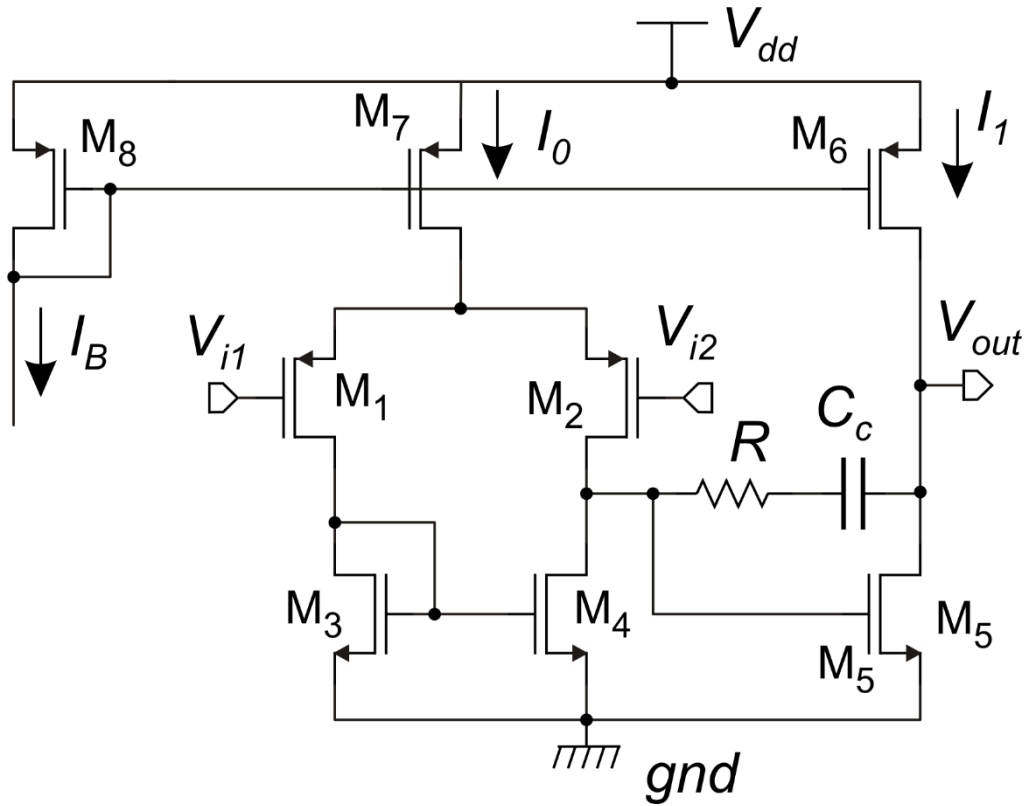# Opamp Design

Specifications

- An offset voltage (absolute value) smaller than 3 mV
- A GBW of 10 MHz for a load capacitance (C_L) up to 10 pF.
- A phase margin around 70° in unity gain configuration

V_{dd}

M_8 M_7 I_0 M_6 I_1

I_B V_{i1} M_1 M_2 V_{i2} C_c V_{out}

R

M_3 M_4 M_5 M_5

gnd

Final component table

	W (µm)	L (µm)			
M_1, M_2	114	0.9	M7	101	3.6
M_3, M_4	10.5	3.6	M8	101	3.6
M_5	94	3.6	R	532 Ω	
M_6	450	3.6	C_C	10 pF	
			I_B	63 µA	