Analog Filter Design

Part. 4: Discrete Time Filters

□ Sect. 4-b: Switched Capacitors Filters

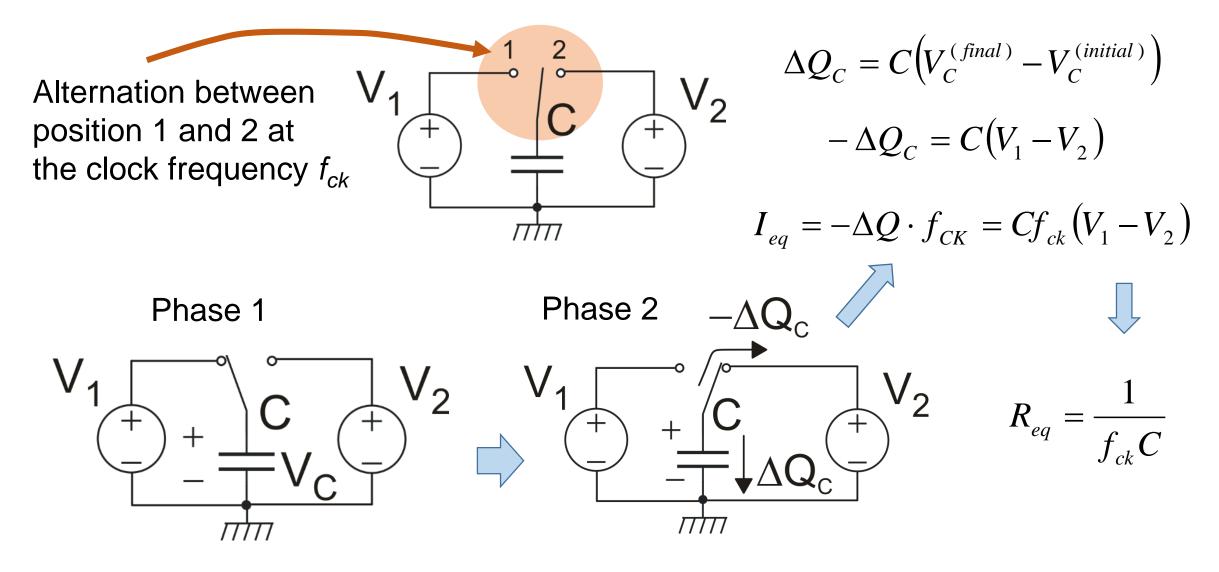
First SC circuits: simulations of resistances

- In integrated RC active filters, the resistances may be the largest components, especially when low frequency singularities are required, as in audio analog processors.
- Singularities in RC filters are proportional to 1/RC factors. Since R and C are marked by non-correlated process variations, the spread in filter characteristic frequencies can be very large (up to 20 %).
- Resistances simulated with switches and capacitors are given by expressions like:

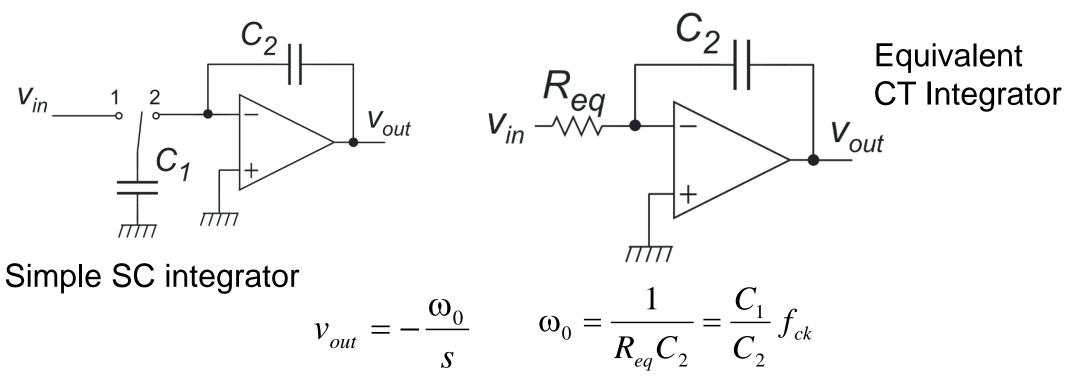
$$R_{eq} = \frac{1}{f_{ck}C}$$

where f_{ck} is the clock frequency. With the small capacitors available on chip it is possible to obtain very large resistors.

Switched Capacitor resistance: principle

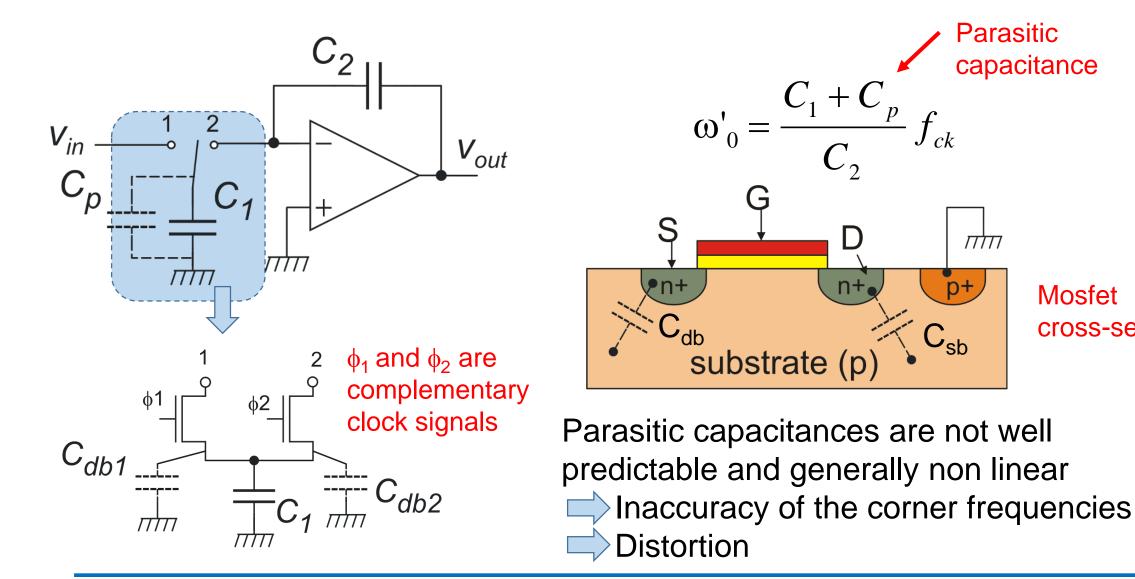


Simple integrator based on SC resistor



Important: The unity gain angular frequency of the integrator (ω₀) depends only on capacitance ratios and the clock frequency. Ratios can be fabricated with high precision and accurate frequencies can be obtained from crystal oscillators
 Filter with precise corner frequencies can be obtained.

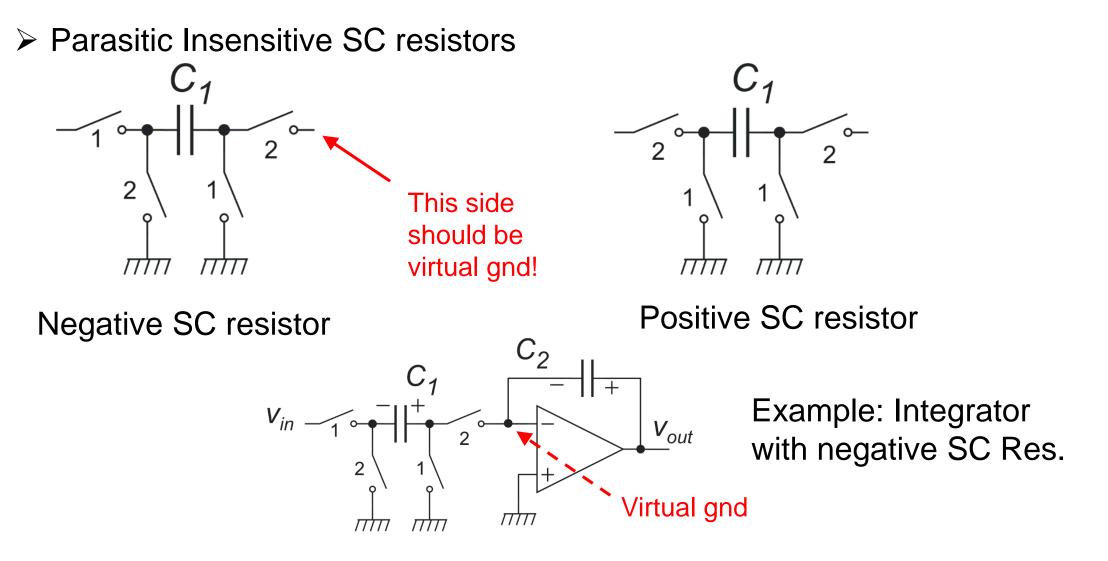
Effect of parasitic capacitances



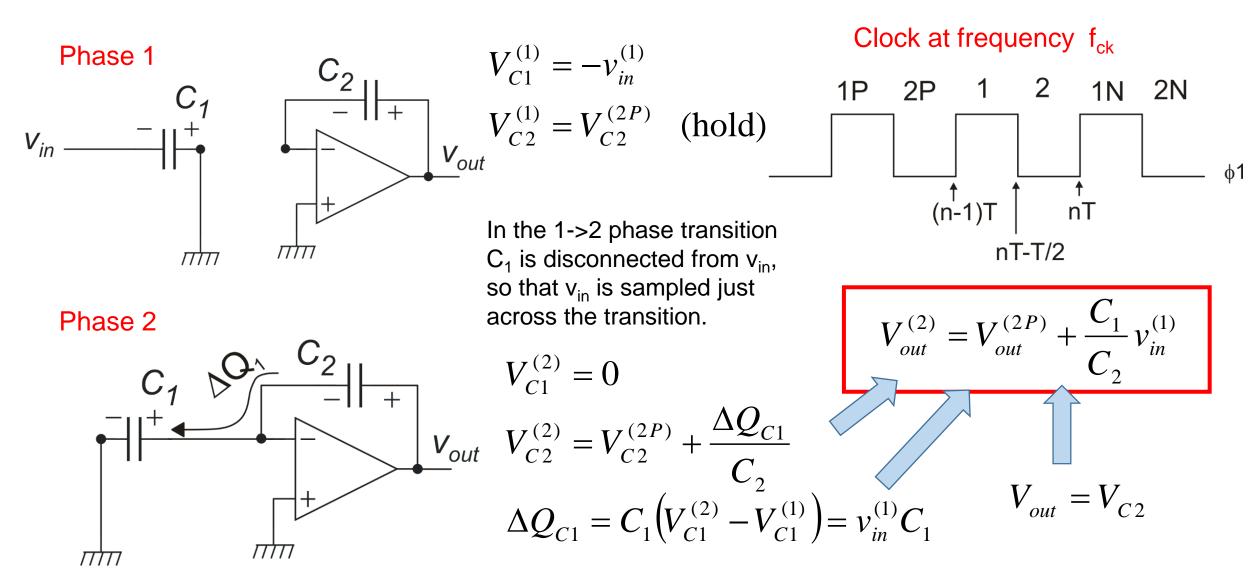
Mosfet

cross-section

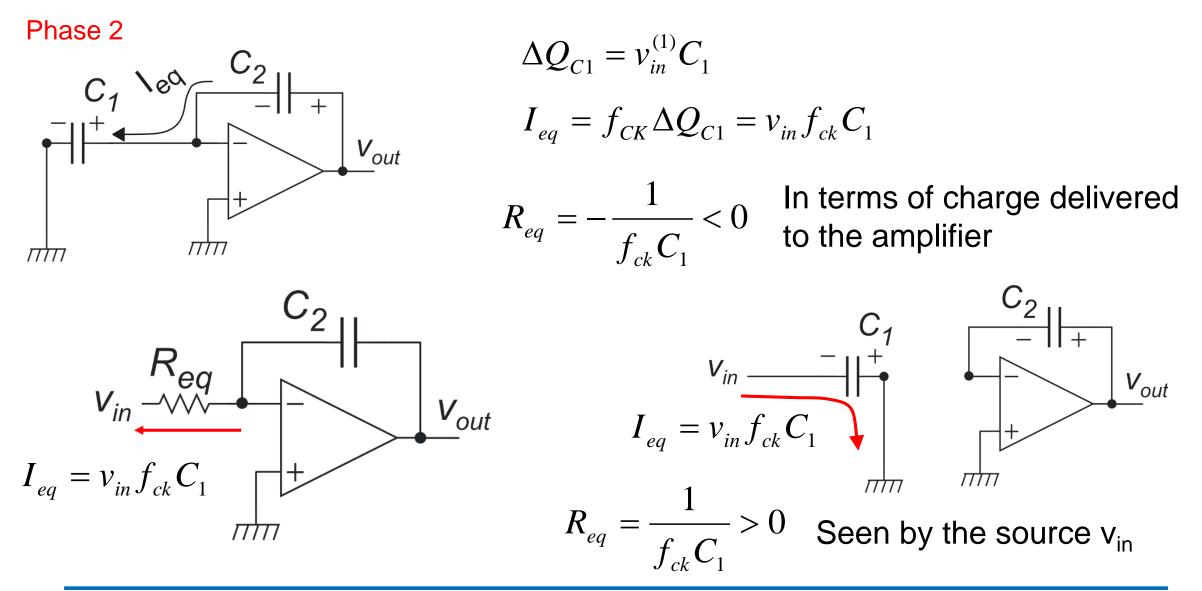
Parasitic Insensitive (PI) SC integrator



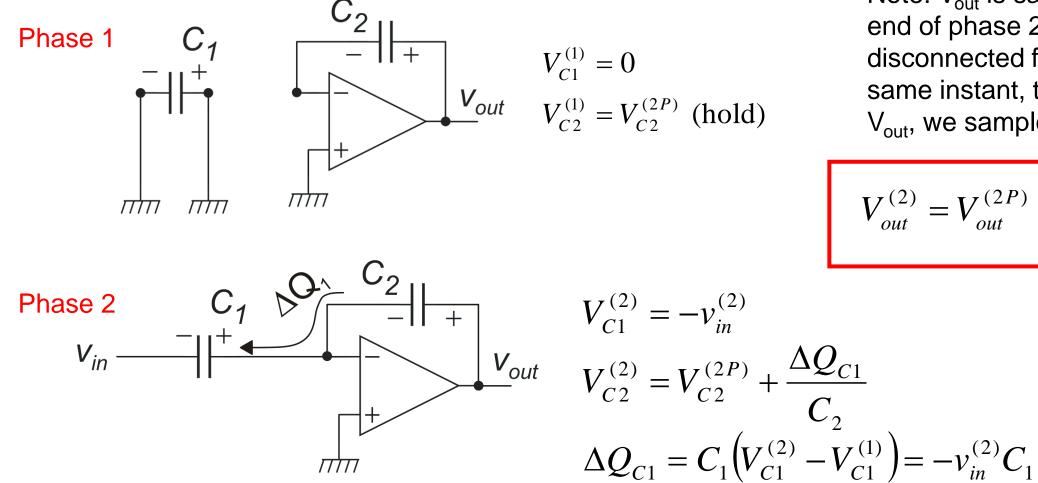
Parasitic Insensitive (PI) SC integrator: negative resistor



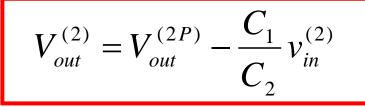
Negative resistor: equivalent currents



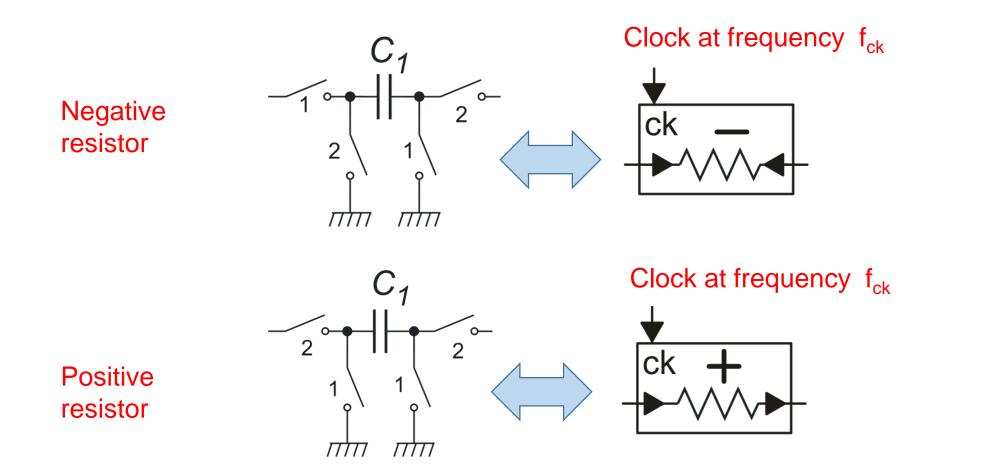
Parasitic Insensitive (PI) SC integrator: positive resistor



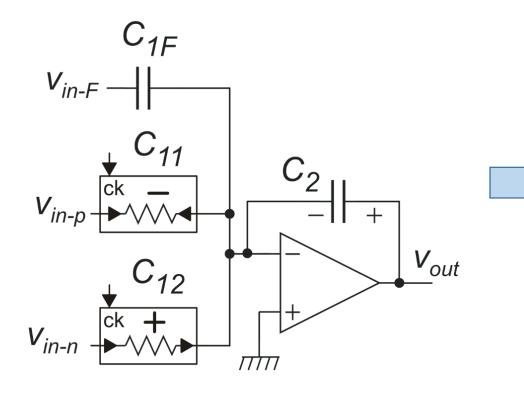
Note: V_{out} is sampled at the end of phase 2, when C_1 is disconnected from v_{in} . In this same instant, together with V_{out} , we sample also v_{in}

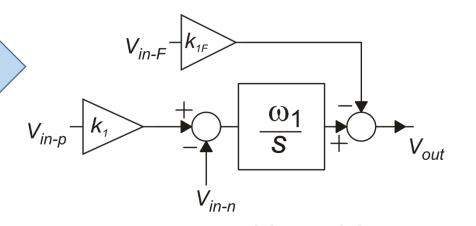


PI-SC resistors: symbols used in this course



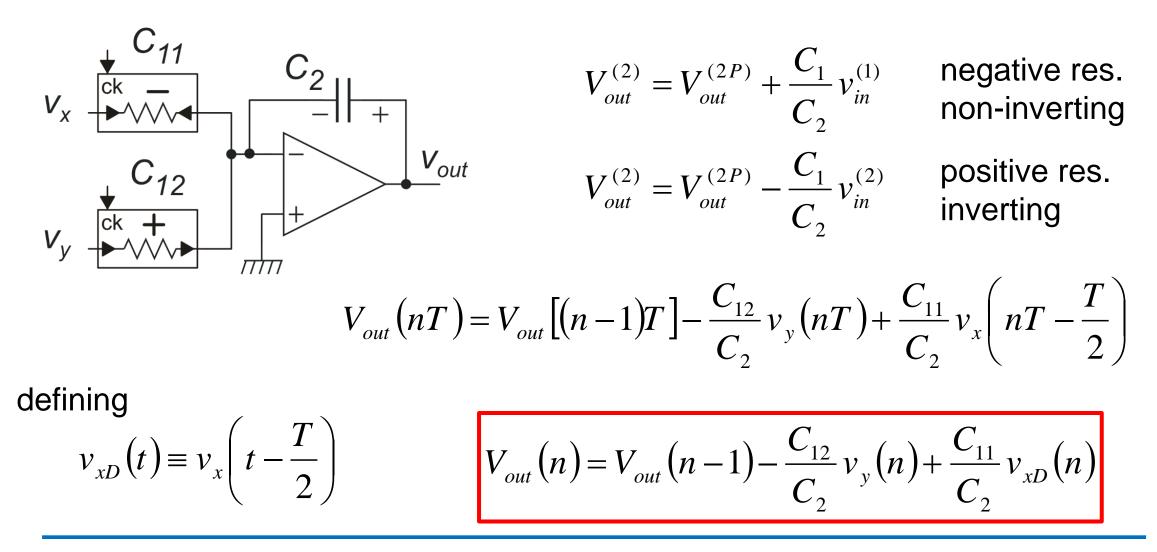
Example 1: versatile integrator





 $\omega_{1} = f \frac{C_{12}}{C_{2}} \qquad k_{1} = \frac{C_{11}}{C_{12}} \qquad k_{1F} = \frac{C_{1F}}{C_{2}} \qquad \text{Note this approximation is valid just when the equivalent resistance model is valid i.e. for f << f_{ck}=f_{s}}$

Discrete time nature of SC filters: Integrator



Discrete time nature of SC filters: Integrator

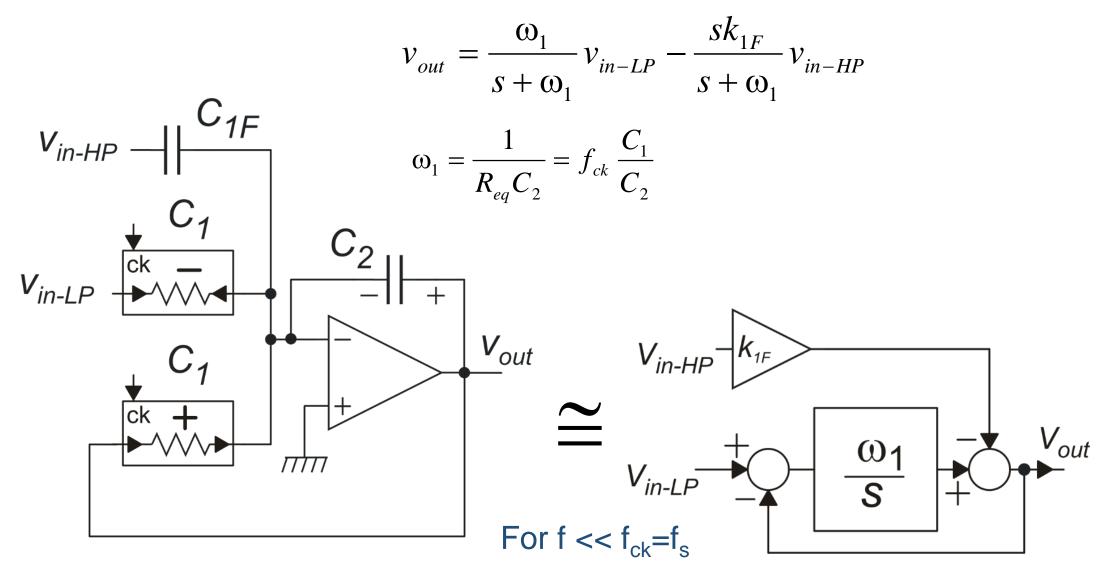
$$V_{out}(n) = V_{out}(n-1) - \frac{C_{12}}{C_2} v_y(n) + \frac{C_{11}}{C_2} v_{xD}(n)$$

$$H(z) = \frac{1}{1-z^{-1}} = \frac{z}{z-1}$$
Backward Euler DT integrator (non-delayed integrator)

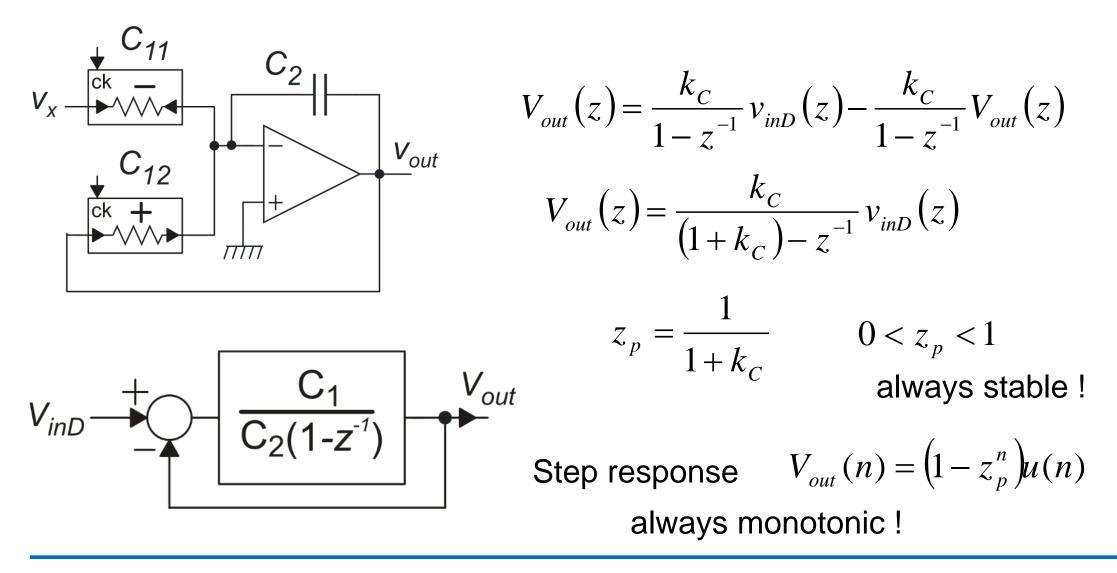
$$V_{out}(z) = z^{-1}V_{out}(z) - \frac{C_{12}}{C_2}V_y(z) + \frac{C_{11}}{C_2}V_{xD}(z)$$
Compare with:

$$V_{out}(z) = \frac{1}{1-z^{-1}} \frac{C_{12}}{C_2}V_y(z) + \frac{1}{1-z^{-1}} \frac{C_{11}}{C_2}V_{xD}(z)$$
Standard (forward) Euler DT integrator (delayed form)

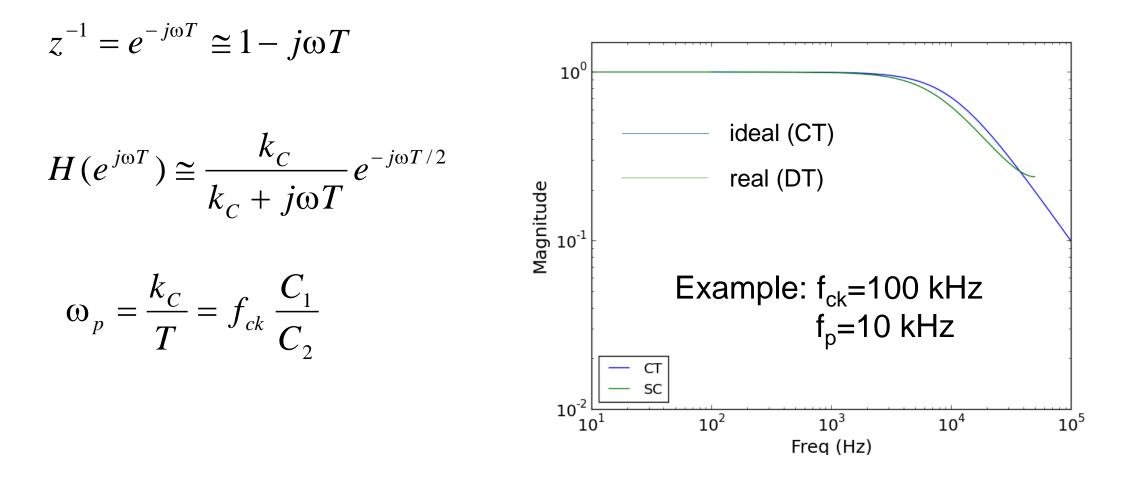
Example 2 First order filter



First order filter: discrete time nature



First order LP filter: frequency response



Example 3: Universal SC Biquad Filter

