

SISTEMI EMBEDDED

Course Presentation – Spring 2016

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Dip. Ing. Informazione

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Office hours:

Friday 15-18. Please, contact me in advance before showing up.

Class Schedule

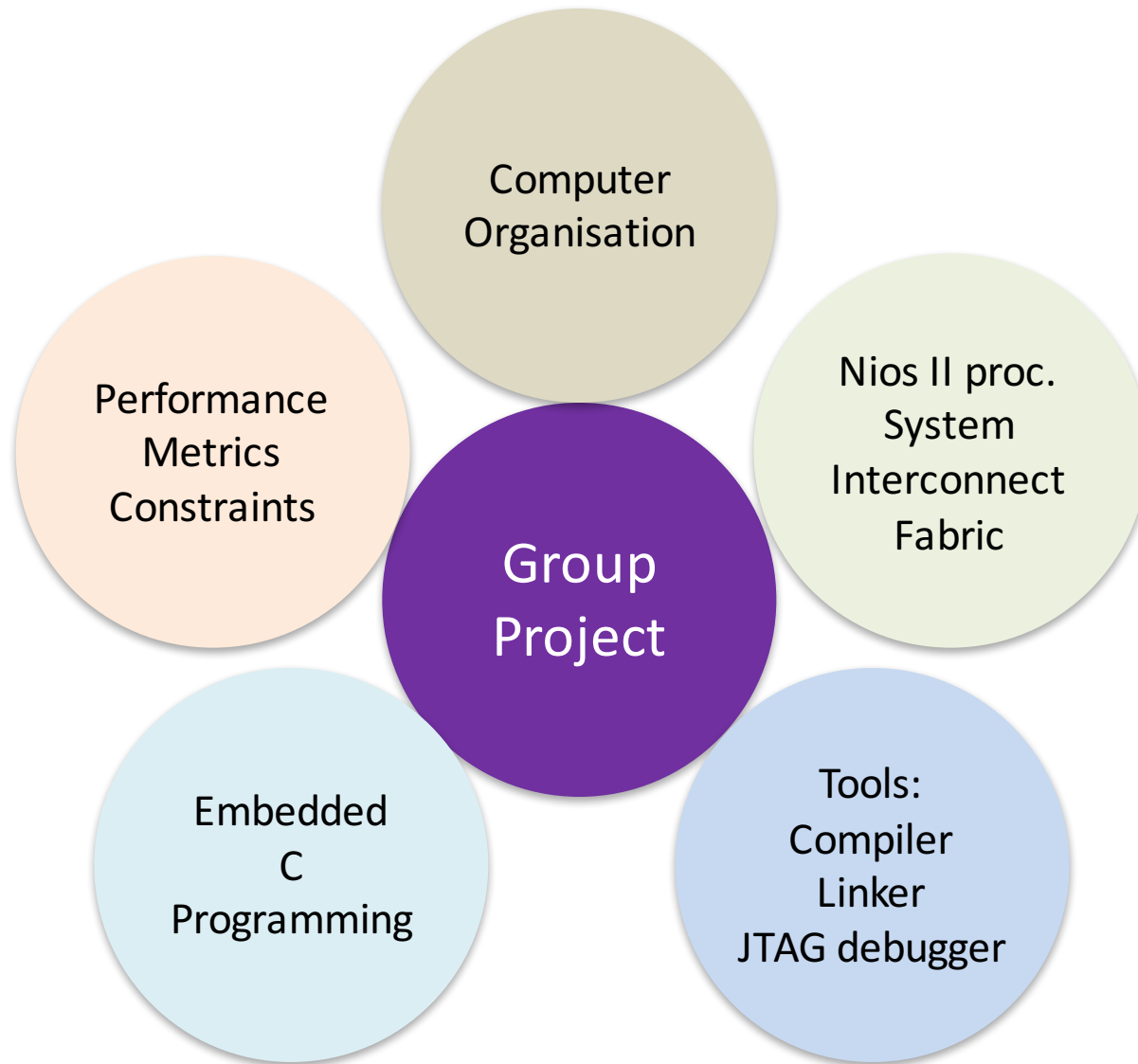
2M Elettronica						
	Lu	Ma	Me	Gi	Ve	Sa
8:30/9:30			Nanoelettronica B23			
9:30/10:30	Nanoelettronica C33		Nanoelettronica B23			
10:30/11:30	Nanoelettronica C33		Sistemi di elaborazione SI 7			
11:30/12:30	Nanoelettronica C33	Sistemi embedded C44	Sistemi di elaborazione SI 7			
12:30/13:30		Sistemi embedded C44	Sistemi di elaborazione SI 7			
13:30/14:30	Sistemi di elaborazione ADInform1					
14:30/15:30	Sistemi di elaborazione ADInform1	Nanoelettronica C41	Sistemi embedded B26			
15:30/16:30	Sistemi embedded B26	Nanoelettronica C41	Sistemi embedded B26			
16:30/17:30	Sistemi embedded B26	Nanoelettronica C41	Sistemi embedded B26			
17:30/18:30	Sistemi embedded B26		Optional Lab			

Course Objectives

Learn expertise and methodologies to design and program an embedded system

We'll use as a reference a **System on Programmable Chip (SoPC)** platform based on an Altera FPGA

Course Organization



Course Requirements

- Digital Electronics
- Digital Logic Design
 - HDL Coding
- Software Programming
 - Basic knowledge of C or C++ Language

Group Project

- Realize an SoPC on a Cyclone II FPGA hosted on the Terasic DE 2 board
 - Computer implementation
 - May require the design of one or more custom peripherals
 - Software programming
- Project assignment during the 5th week (after Easter break)
- See Projects of previous years to gather an idea

Course Material (1)

- Available at the course web page:

<http://www.iet.unipi.it/f.baronti/didattica/SE/2016/Sistemi%20Embedded.html>

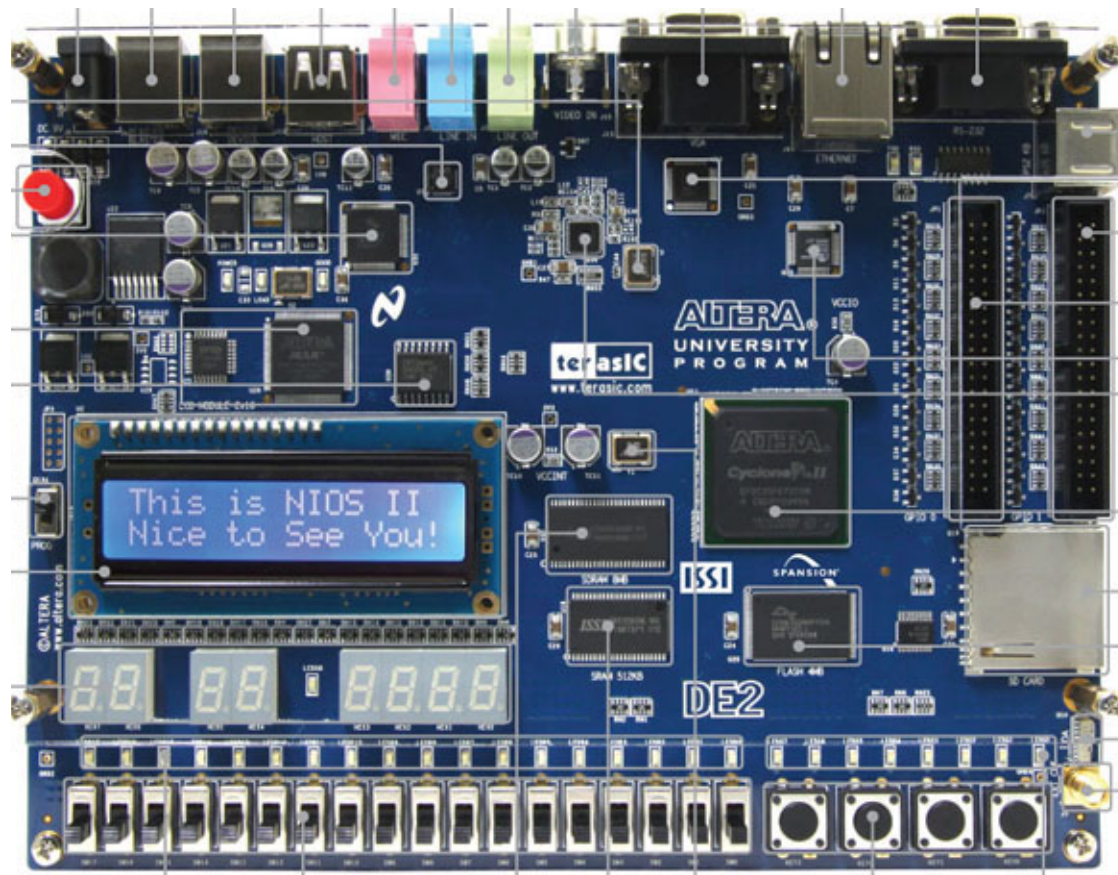
- Slides used during the lessons
- Documentation from Altera
- Suggested book:
 - C. Hamacher, Z. Vranesic, S. Zaky, N. Manjikian
"Computer Organization and Embedded Systems,"
McGraw-Hill International Edition

Course Material (2)

- Quartus II Web Edition Software (**13.0sp1**)
<http://dl.altera.com/13.0sp1/>
 - Quartus II Software (includes Nios II EDS)
 - ModelSim-Altera Edition (includes Starter Edition)
 - Devices: Cyclone II, Cyclone III, Cyclone IV device support (includes all variations)
- University Program Installer (**13.0**)
 - <https://www.altera.com/support/training/university/materials-software.html> - University-Program-Installer
- Nios II Documentation
 - <http://www.altera.com/literature/lit-nio2.jsp>

Course Material (3)

- DE2: Development & Education board
 - Cyclone II EP2C35F672C6 (33216 LE; 105 M4K)



Exam Evaluation

- Project Evaluation (~40 %): the same marking for all the group members
 - Presentation and Demo (~30 %) - “Hopefully” on last class
 - Project report (~70 %) – Due a “week” before the selected exam date
- Oral Test (~60 %)
 - Project discussion (~50 %)
 - Assessment of understanding and mastering the course content (~50 %)